

Application Number 10/693,012
Response to Office Action mailed February 21, 2008

RECEIVED
CENTRAL FAX CENTER

JUN 23 2008

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0054] of Applicant's originally-filed disclosure with the following amended paragraph:

[0054] FIG. 8 is a flow diagram illustrating operation of the power management module 38 of FIG. 4. As shown in FIG. 8, power management module 38 senses the input voltage V_IN (84), compares the input voltage V_IN to a threshold voltage V_TH (86). More specifically, power management module 38 determines whether the input voltage V_IN is greater than (as indicated by “>” in FIG. 8) the threshold voltage V_TH. If the input voltage V_IN exceeds (i.e., is greater than) the threshold voltage V_TH, e.g., approximately 2.4 to 2.6 volts, power management module 38 disables an input transistor such as MOSFET 60 (FIG. 4) to reduce the input voltage and thereby prevent pulse skipping in boost converter 56 (88). If the input voltage V_IN does not exceed (i.e., is not greater than) the threshold voltage V_TH, power management module 38 enables the input transistor to transmit the input voltage V_IN to boost converter 56 (90).